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CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the below date:

Date: August 15, 2006

Name: Sid Bennett

Signature:

BRINKS  
HOFER  
GILSON  
& LIONE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Shawn D. ROGERS et al.	Examiner:	
Serial No.:	10/802,664	Dickey, Thomas L.	
Filed:	03/17/2004	Art Unit:	2826
For:	CIRCUIT AND METHOD FOR ENHANCED LOW FREQUENCY SWITCHING NOISE SUPPRESSION IN MULTILAYER PRINTED CIRCUIT BOARDS USING A CHIP CAPACITOR LATTICE		
Attorney Docket No:	13050-12		

TRANSMITTAL

Mail Stop: Amendment  
Commissioner for Patents  
Alexandria, VA 22313-1450

Sir:

Attached are:

- ☒ Transmittal Letter (in dup); Response to Notice of Non-Compliant Amendment (17 p); and
- ☒ Return Receipt Postcard

Fee calculation:

- ☐ An extension fee in the amount of \$\_\_\_ for a \_\_\_-month extension of time under 37 C.F.R. § 1.136(a).
- ☐ An additional filing fee has been calculated as shown below:

					Small Entity			Not a Small Entity	
	Claims Remaining After Amendment		Highest No. Previously Paid For	Present Extra	Rate	Add'l Fee	or	Rate	Add'l Fee
Total		Minus			1 x \$25=			x \$50=	
Indep.		Minus			2 x 100=			x \$200=	
First Presentation of Multiple Dep. Claim					+\$180=			+\$360=	
					Total			Total	\$

Fee payment:

- ☐ A check in the amount of \$0 is enclosed.
- ☐ Please charge Deposit Account No. 23-1925 in the amount of \$ . A copy of this Transmittal is enclosed for this purpose.
- ☒ The Director is hereby authorized to charge payment of any additional filing fees required under 37 CFR § 1.16 and any patent application processing fees under 37 CFR § 1.17 associated with this paper (including any extension fee required to ensure that this paper is timely filed), or to credit any overpayment, to Deposit Account No. 23-1925.

Respectfully submitted,

Sid Bennett (Reg. No. 53,981)  
Agent for Applicants

15-VII-02  
Date



Serial No.: 10/802,664

hereby certify that this correspondence is being deposited with the United States Postal Service, with sufficient postage, as first class mail in an envelope addressed to:

M/S Amendment,  
Commissioner for Patents  
P.O. Box 1450, Alexandria, VA 22313-1450  
on August 15, 2006

Date of Deposit

Sid Bennett, Reg. No. 53,981

Name of applicant, assignee or  
Registered Representative

Signature

Date of Signature

Our Case No. 13050-12

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shawn D. ROGERS et al.

Serial No. 10/802,664

Filing Date: 03/17/2004

For: CIRCUIT AND METHOD FOR ENHANCED  
LOW FREQUENCY SWITCHING NOISE  
SUPPRESSION IN MULTILAYER PRINTED  
CIRCUIT BOARDS USING A CHIP  
CAPACITOR LATTICE

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) Examiner: Dickey, Thomas L.  
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) Art Unit: 2826  
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## RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

M/S Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action dated July 21, 2006.  
The Claims begin on page 3, and the Remarks begin on page 14. This paper

Serial No.:10/802,664

corrects and replaces the paper submitted on June 27, 2006, by adding new Claim 53 which had been omitted.